Specification for Electroless Nickel/Immersion Gold (ENIG) Plating for Printed Circuit Boards

Developed by the Plating Processes Subcommittee (4-14) of the Fabrication Processes Committee (4-10) of IPC

Users of this standard are encouraged to participate in the development of future revisions.

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1 SCOPE

1.1 Scope  This specification sets the requirements for the use of Electroless Nickel/Immersion Gold (ENIG) as a surface finish for printed circuit boards. This specification is intended to set requirements for ENIG deposit thicknesses based on performance criteria. It is intended for use by supplier, printed circuit manufacturer, electronics manufacturing services (EMS) and original equipment manufacturer (OEM).

1.2 Description  ENIG is an electroless nickel layer capped with a thin layer of immersion gold. It is a multifunctional surface finish, applicable to soldering, aluminum wire bonding, press fit connections, and as a contact surface. The immersion gold protects the underlying nickel from oxidation/passivation over its intended life. However, this layer is not totally impervious and it will not pass the requirements of a ‘classic’ porosity test.

1.2.1 Phosphorus/Boron Content  Phosphorus or boron containing reducing agents are used for the reduction of the electroless nickel during the deposition process. Phosphorus or boron is thus incorporated in the nickel deposit. The level of these co-deposited elements should be controlled within the specified process limit. Variation of phosphorus or boron level, outside the specified process limits, may have adverse effects on the solderability of the finish.

1.3 Objective  This specification sets the requirements specific to ENIG as a surface finish (see Table 3-1 for a summary of these requirements). As other finishes require specifications, they will be addressed by the IPC Plating Processes Subcommittee as part of the IPC-4550 specification family. As this and other applicable specifications are under continuous review, the subcommittee will add appropriate amendments and make necessary revisions to these documents.

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<th>Table 3-1</th>
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(1) An appropriate IPC-TM-650 test method used to generate data for this electrical property is not available at the time of this writing.